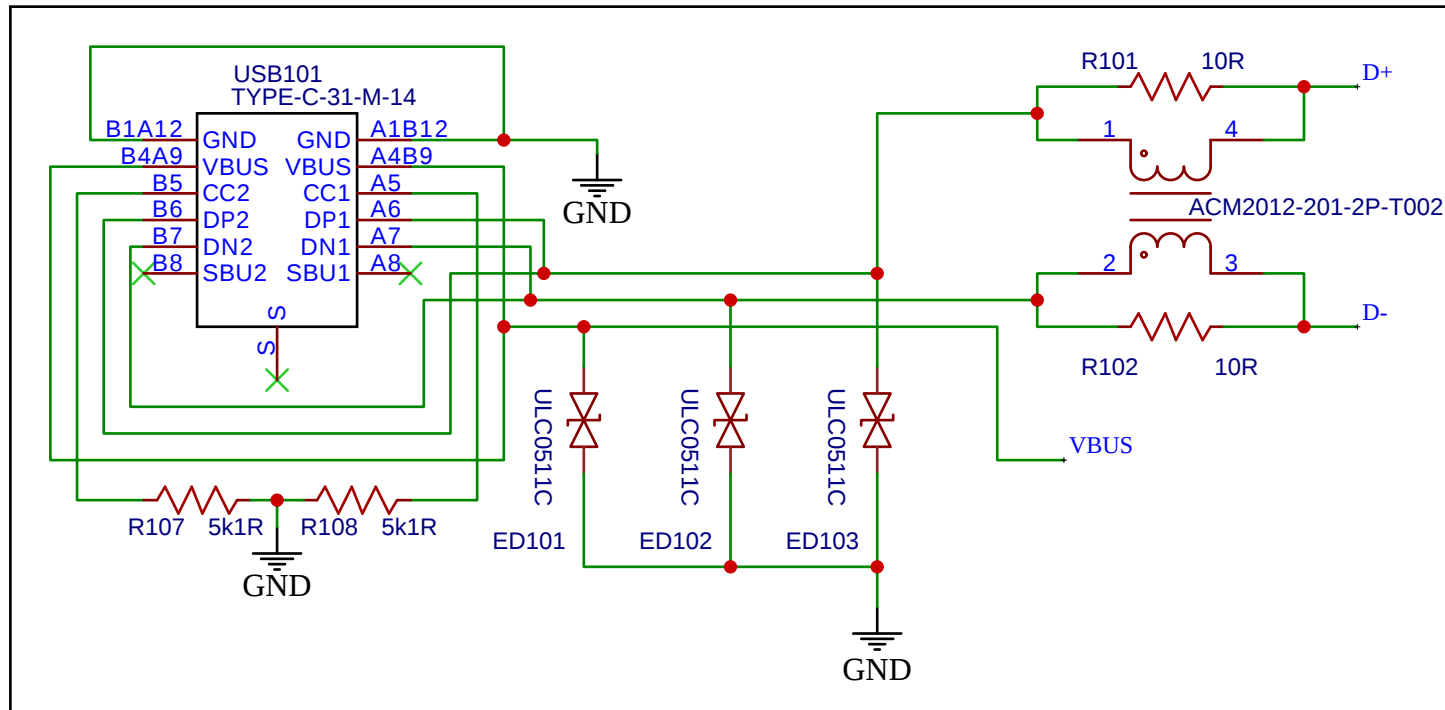
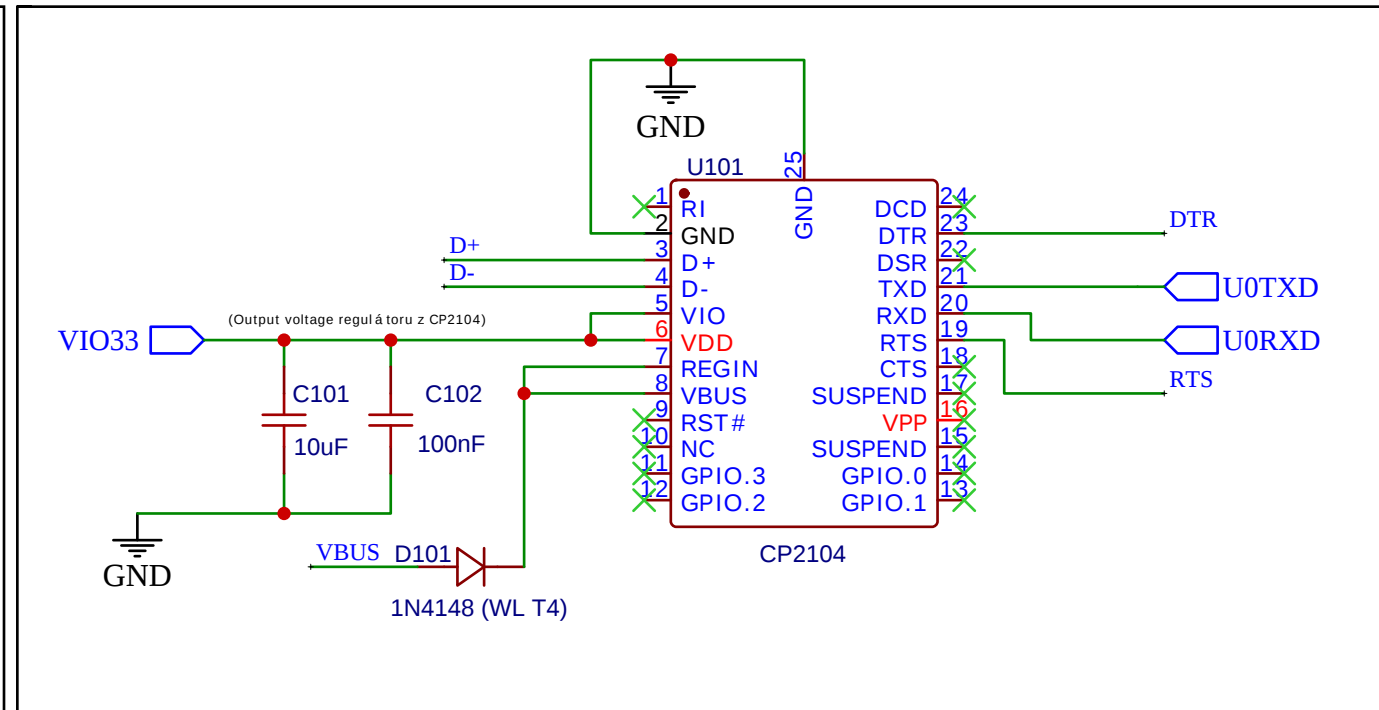


The diagram shows a voltage divider circuit. A 5V source is connected to a 10k resistor (R205) in series with a 100nF capacitor (C205) to ground. The other end of R205 is connected to the VIN pin of an ME6211 buffer (U202). The VSS pin is connected to ground, and the CE pin is connected to a 10k resistor (R205) to ground. The VOUT pin is connected to a 100nF capacitor (C204) to ground and a 22uF capacitor (C206) to VCC. The NC pin is marked with a green X.

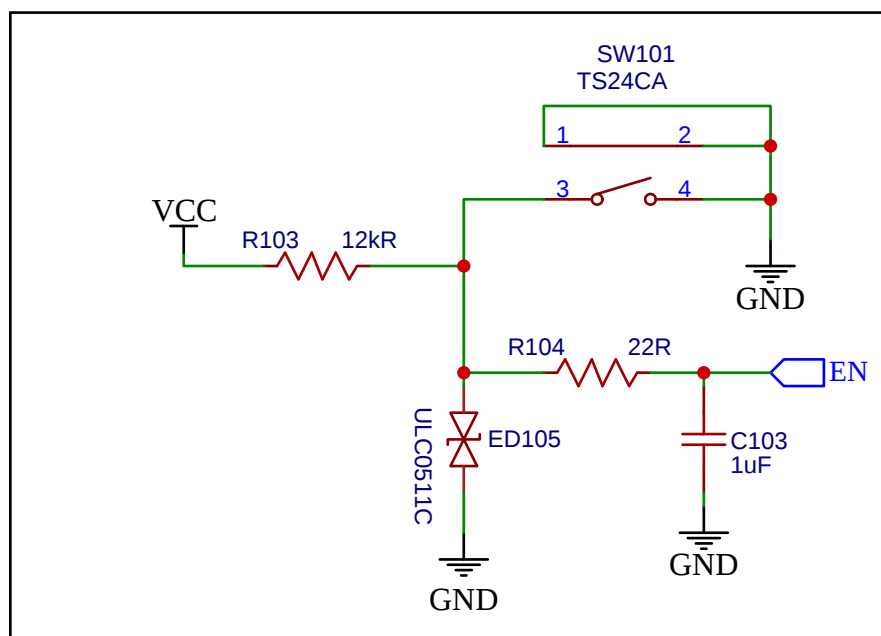
# USB



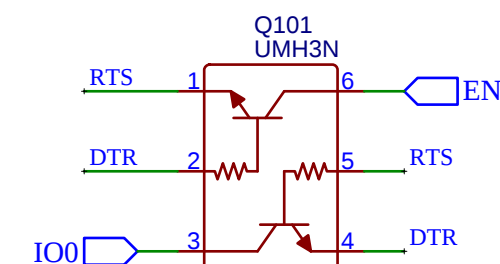
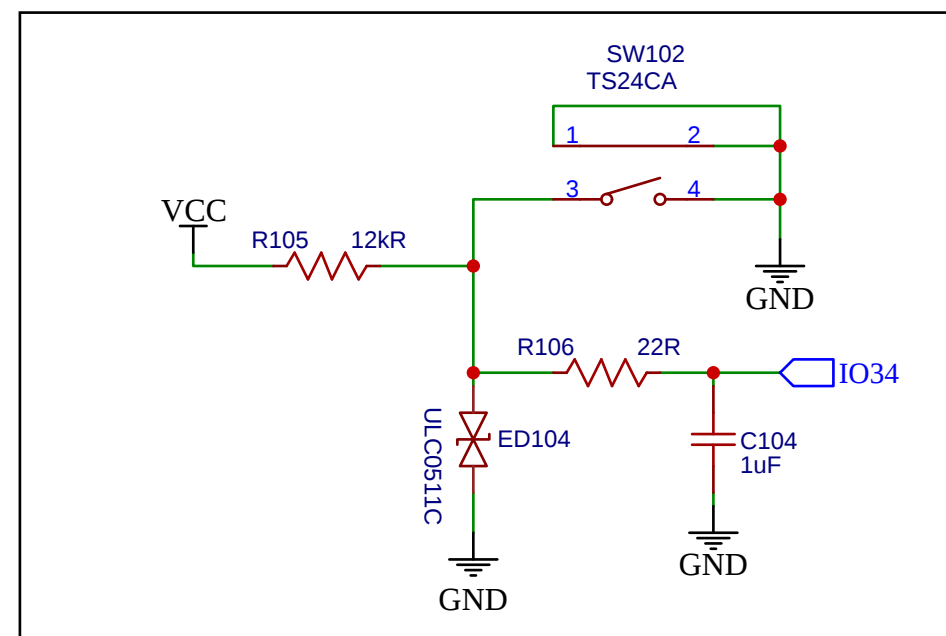
# UART



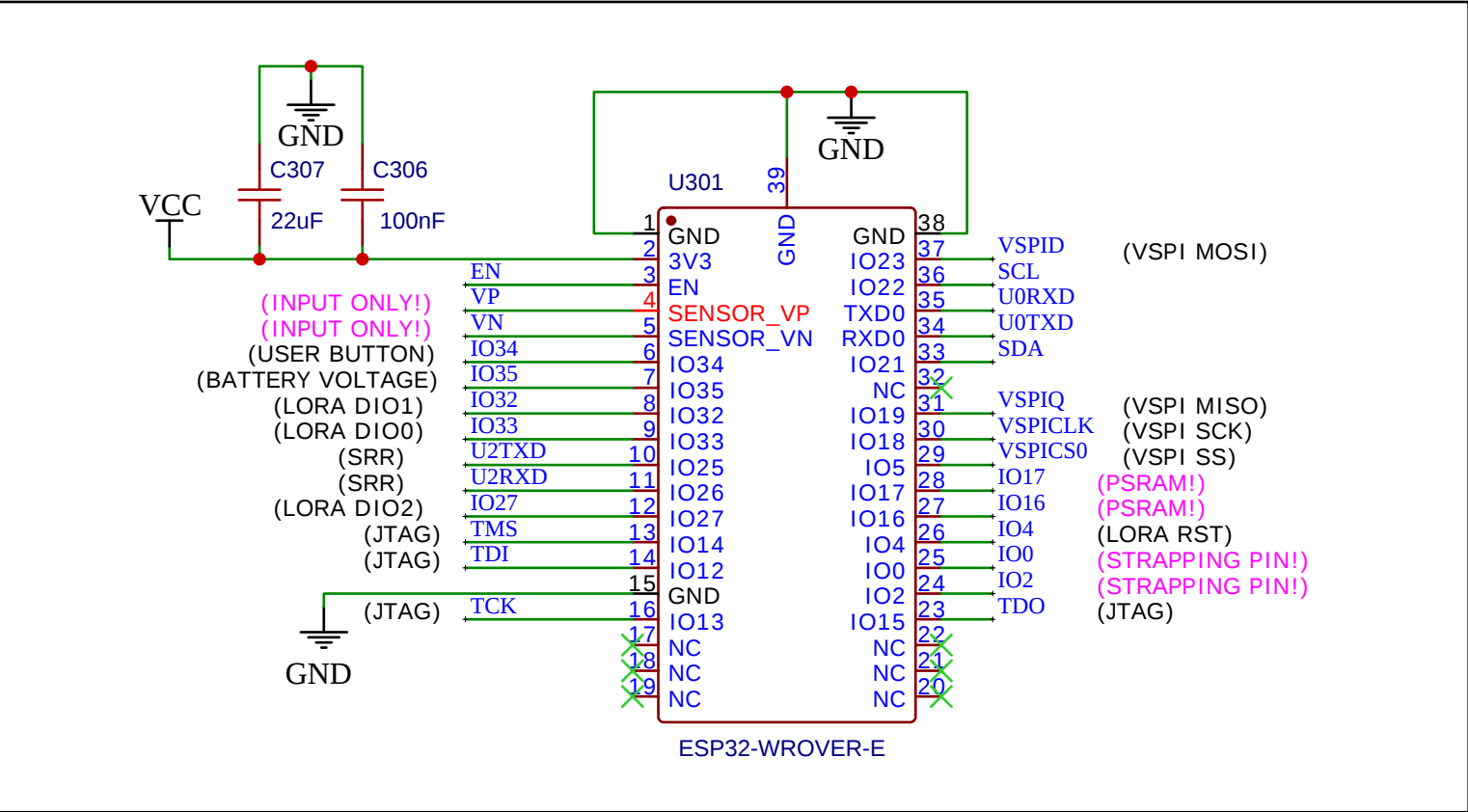
# RESET BUTTON



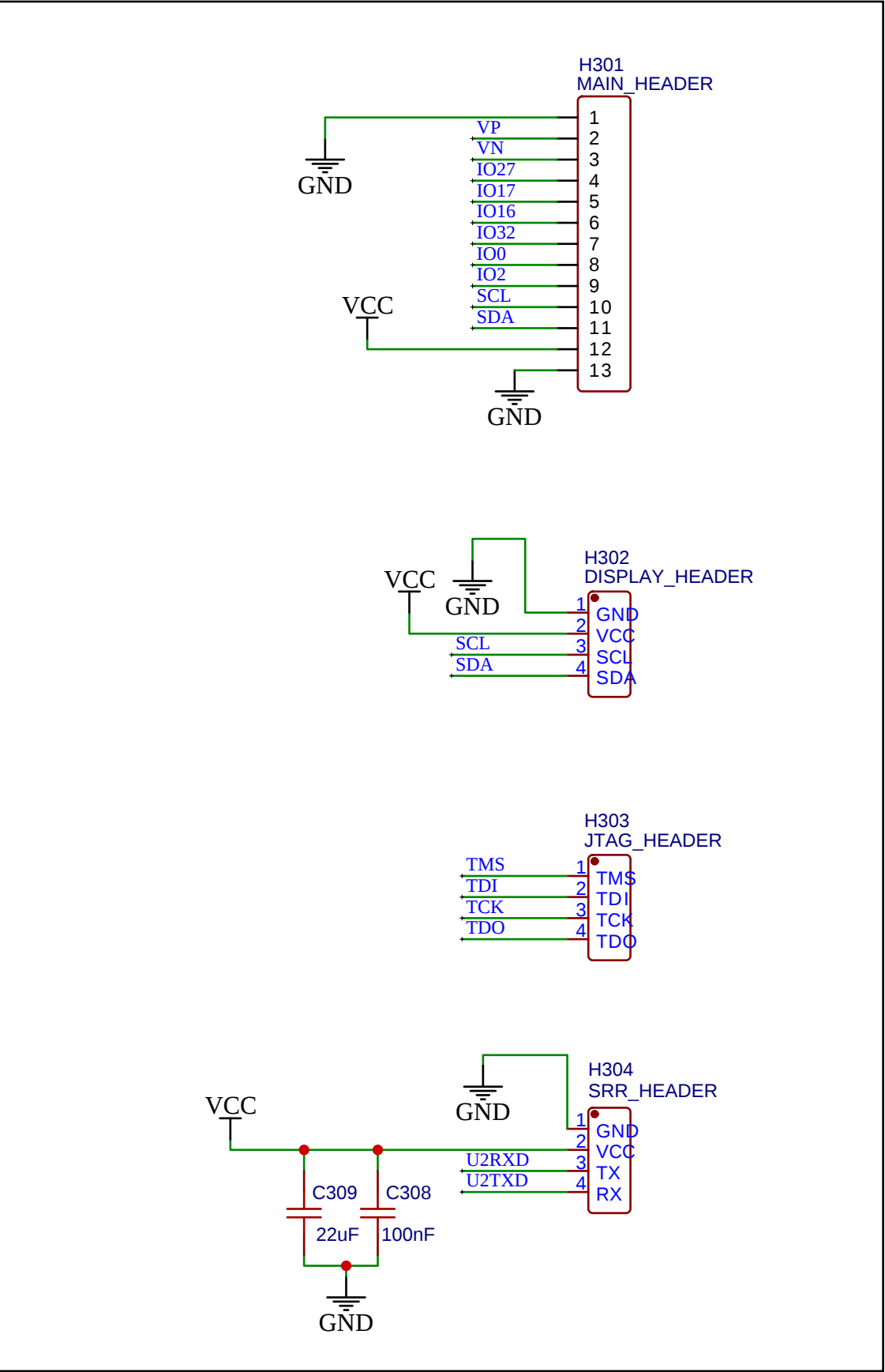
# USER BUTTON



ESP32



HEADERS



LORA

